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APPLICANTS

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** CONTINUING DATA *****
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** FOREIGN APPLICATIONS *****
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IF REQUIRED, FOREIGN FILING LICENSE GRANTED
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35 USC 119 (a-d) conditions met ☒ yes ☐ no ☐ Met after Allowance

Verified and Acknowledged *[Signature]* *[Initials]*

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TITLE
 Method for producing a vertical semiconductor transistor component and vertical semiconductor transistor component

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